



## Micron's Scott Graham to Keynote at MemCon Memory Conference 2012

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BOISE, Idaho, Sept. 11, 2012 (GLOBE NEWSWIRE) -- Micron Technology, Inc. (Nasdaq:MU), one of the world's leading providers of advanced semiconductor solutions, today announced that Scott Graham, general manager of Micron's Hybrid Memory Cube technology, will be a keynote speaker at the MemCon Memory Conference on Tuesday, September 18, at the Santa Clara Convention Center in Santa Clara, California. Based on 20 years of semiconductor industry experience with DRAM and NAND memory products, Graham will explain how hybrid 3D architectures are enabling the high-performance computing world, providing greater bandwidth and energy efficiency than any existing memory technology.

Micron's Hybrid Memory Cube is the first to successfully combine advanced logic with DRAM process technology using through-silicon vias, and it is scheduled for volume production the second half of 2013.

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### About Micron

Micron Technology, Inc., is one of the world's leading providers of advanced semiconductor solutions. Through its worldwide operations, Micron manufactures and markets a full range of SSDs, DRAM, NAND and NOR flash memory, as well as other innovative memory technologies, packaging solutions and semiconductor systems for use in leading-edge computing, consumer, networking, embedded and mobile products. Micron's common stock is traded on the NASDAQ under the MU symbol. To learn more about Micron Technology, Inc., visit [www.micron.com](http://www.micron.com).

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### About MemCon Memory Conference

The conference is designed for CEOs, CTO, VPs, engineering management, system architects, product managers, chip designers, and software developers to gain access to exclusive presentations from leading industry experts who address memory sub-system development, device roadmaps, and strategies for integrating memory and storage into new consumer electronic applications. MemCon includes focused, interactive presentation and panel sessions that address the challenges of enabling optimal memory and storage systems for the next generation of electronics. For more information, visit <http://www.memcon.com/>

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