micron

Micron Unveils Serial NOR Flash Interface With Replay-Protected Monotonic Counter for Ultrathin Devices

September 10, 2013 at 12:00 AM EDT

Industry's First SPI NOR Device With Replay-Protected Monotonic Counter Feature Meets Security Requirements of Ultrathin Applications and is Validated for Future Intel® Ultrabook™ Platforms

BOISE, Idaho, Sept. 10, 2013 (GLOBE NEWSWIRE) -- Micron Technology, Inc., (Nasdaq:MU) today announced the availability of a replay-protected monotonic counter (RPMC) feature for their SPI NOR Flash memory devices, which are validated for future Intel Ultrabook platforms.

The cost-effective 64Mb density is the sweet-spot solution currently available for immediate platform-enablement activities.

The RPMC feature in Micron's SPI NOR device is the first in a family of cryptographic primitives that will significantly enhance preboot security in cost-sensitive embedded, mobile, and personal computing architectures. The RPMC-enabled device facilitates critical nonvolatile data storage, while making systems resistant to rollback and replay attacks. It enables original equipment manufacturers (OEMs) to further strengthen code/data storage in the boot memory and deliver more secure systems to customers.

Micron's 64Mb RPMC-enabled SPI NOR device supports nonvolatile storage and authentication needs that are critical to the chipset security implementation for future Intel Ultrabook platforms and is compliant with [Intel's Serial Flash Hardening Product External Architecture Specification](#). The device, which is available in SO8W and W-DFN 0.8mm packages, also includes improved erase performance to increase throughput and lower the initial cost of manufacturing programming.

"This first-to-market RPMC feature, developed by Micron for our SPI NOR device, is an additional demonstration of our commitment to continuously meet application requirements and customer needs," saidJeff Bader, vice president of Micron's Embedded Solutions Marketing. "Our NOR Flash excellence, coupled with the leadership and expertise of Intel, make this device the best-in-class solution for the Intel Ultrabook and other future Intel-based platforms."

"I appreciate Micron's expertise in the SPI Flash device technology space and their support of the replay-protected monotonic counter feature for the Intel platform prototype validation," said Nitin Sarangdhar, Sr. Principal Engineer at Intel.

The 64Mb serial NOR Flash with RPMC feature is available now in production quantities.

With this announcement, Micron is well-positioned to support the growth of ultrathin applications with a broad portfolio of DRAM, NAND, NOR, and SSD solutions. For more information, please visit http://www.micron.com/products/topics/ultrathin-solutions.

Follow us online! Take part in Micron's social conversations where we're talking all things memory:

- Blog: www.micron.com/about/blogs
- LinkedIn: www.linkedin.com/company/micron-technology

About Micron

Micron Technology, Inc., is one of the world's leading providers of advanced semiconductor solutions. The company transforms foundational memory technologies—including DRAM, NAND Flash, and NOR Flash—into innovative solutions such as solid state drives (SSDs) and storage appliances, modules, multichip packages, and other semiconductor systems. Micron's worldwide operations design, manufacture, and market these solutions for use in leading-edge computing, consumer, enterprise servers and storage, networking, embedded, and mobile products. Micron's common stock is traded on the NASDAQ under the MU symbol. To learn more about Micron Technology, Inc., visit www.micron.com.

```
CONTACT: PR CONTACT:

        Mary Ellen Ynes

        Zeno Group for Micron Technology

        650-801-7954

        MaryEllen.Ynes@zenogroup.com
```