



Contact: John Schroeter
Pico Computing
506 Second Avenue
Seattle, WA 98104
Phone: (206) 283-2178
jschroeter@picocomputing.com

Pico Computing Joins Hybrid Memory Cube Consortium

Company Recognized as Early Adopting Member

Seattle, WA – February 11, 2012 – Pico Computing, a recognized leader in hardware accelerated FPGA solutions, announced today its membership to the Hybrid Memory Cube Consortium (HMCC). Led by Micron Technology, Inc. and Samsung Electronics Co., Ltd, the HMCC is composed of many of the world's leading technology innovators.

Over the past decade, the amount of collected data, the speed with which it is being amassed, and the complexity in analyzing information such as that held in DNA strands, has begun to overwhelm the computational resources required to process it. To keep pace, new memory resources with higher bandwidth and new processing techniques are required.

The Hybrid Memory Cube (HMC) is a highly efficient, next generation memory system designed to help accelerate computationally intensive applications. This revolutionary integrated circuit architecture features a base layer of logic responsible for controlling the dense stacks of vertical DRAM located above. The HMC offers shorter latencies, energy efficiency and a reduced physical form factor while dramatically increasing memory bandwidth and density.

Pico Computing is proud to join the HMCC's efforts in developing the Hybrid Memory Cube and to help provide higher performance solutions to these customers.

About the HMCC

Founded by leading members of the world's semiconductor community, the Hybrid Memory Cube Consortium (HMCC) is dedicated to the development and establishment of an industry-standard interface specification for the Hybrid Memory Cube technology. Members of the consortium include Altera Corporation, ARM, HP, IBM, Micron Technology, Microsoft Corporation, Open-Silicon, Inc., Pico Computing, Samsung Electronics Co., Ltd., SK hynix Co., and Xilinx, Inc. More than 115 prospective adopters are exploring consortium membership. To learn more about the HMCC, visit www.hybridmemorycube.org

About Pico Computing

Pico Computing is the technology leader in high-performance computing. Our modular, highly scalable HPC and embedded systems solve the biggest of the big data computing challenges—from the edge to the data center to the desktop. Whether targeted to PCI Express-based HPC or standalone embedded applications, Pico Computing's massively-scalable architecture, built upon Field Programmable Gate Array (FPGA) technologies, brings orders-of-magnitude performance gains, greatly reduced energy costs, the industry's smallest form factors, and simplified application design. To learn more about Pico Computing, visit www.picocomputing.com

###