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New High Performance Computing Module Eases Design and Development Efforts While Enabling Low-cost Applications

Company Announces new M-505K160T Module

Seattle, WA - April 23, 2013 - The newest addition to Pico Computing's expanding line of application-optimized HPC and embedded modules is the M-505K160T. Based on the Xilinx Kintex™ K160T FPGA, the new module is targeted to applications that require high performance with low power at an entry-level price point.

In addition to meeting this trifecta of engineering constraints, the M-505K160T fills a significant market void by providing a truly plug-and-play solution. Pico Computing's modular and highly scalable architecture makes expanding the computing bandwidth as simple as plugging additional modules into the backplane. Startup and proof-of-concept projects in particular will benefit from this approach, being thus freed from the budget penalties imposed by other solution providers whose systems are large, expensive, and do not scale efficiently.

When deployed as part of a PCI-Express-based HPC system (built upon Pico Computing's EX-Series backplanes), the M-505K160T is supported by Pico Computing's development framework. When used in a standalone embedded application, development is supported via Pico Computing's design package that includes mechanical, electrical, and interface specifications.

The M-505K160T integrates an 8GB DDR3 SODIMM, providing up to 12.8GB/s of local memory bandwidth to the FPGA. Communication with the host is managed via x8 Gen2 PCIe. Using two I/O connectors, located on the front and backside of the board, the M-505K160T provides 40 LVDS lines for increased flexibility in embedded systems. The included Pico Computing development framework contains host side drivers, APIs, interfaces to host and memory, and tutorials that allow engineers to get up to speed quickly. All Pico Computing products receive dedicated applications support to ensure customer success.

About Pico Computing
Pico Computing is the technology leader in high-performance computing. Our modular, highly scalable HPC and embedded systems solve the biggest of the big data computing challenges-from the edge to the data center to the desktop. Whether targeted to PCI Express-based HPC or standalone embedded applications, Pico Computing's massively-scalable architecture, built upon Field Programmable Gate Array (FPGA) technologies, brings orders-of-magnitude performance gains, greatly reduced energy costs, the industry's smallest form factors, and simplified application design. To learn more about Pico Computing, visit www.picocomputing.com

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Contact: John Schroeter
Pico Computing 506 Second Avenue
Seattle, WA 98104
Phone: (206) 283-2178
jschroeter@picocomputing.com